

EL 844016303

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Priority Application Serial No. 08/842,230
Priority Filing Date April 22, 1997
Inventor Luan Tran et al.
Assignee Micron Technology, Inc.
Priority Group Art Unit 2814
Priority Examiner H. Weiss
Attorney's Docket No. MI22-1784
TITLE: Memory Integrated Circuitry

Assistant Commissioner for Patents
Washington, D. C. 20231
Attention: Official Draftsman

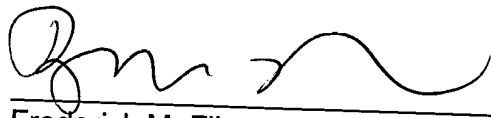
SUBSTITUTE DRAWING REQUEST

Enclosed is a Red-line drawing of Fig. 2 along with a corrected formal drawing, and substitute formal drawings, Figs. 1-3. Please enter the enclosed substitute formal drawings in the above-referenced application in place of drawings originally filed. The content of the drawings are identical to those now on file in this application.

Acknowledgment of receipt of the formal drawings and their acceptance into the file is requested.

Respectfully submitted,

Date: Aug. 14, 2001

By: 
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Enclosures: 1 Red-line Drawing, Fig. 2 and 2 Sheets of Formal Drawings, Figs. 1-3.

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